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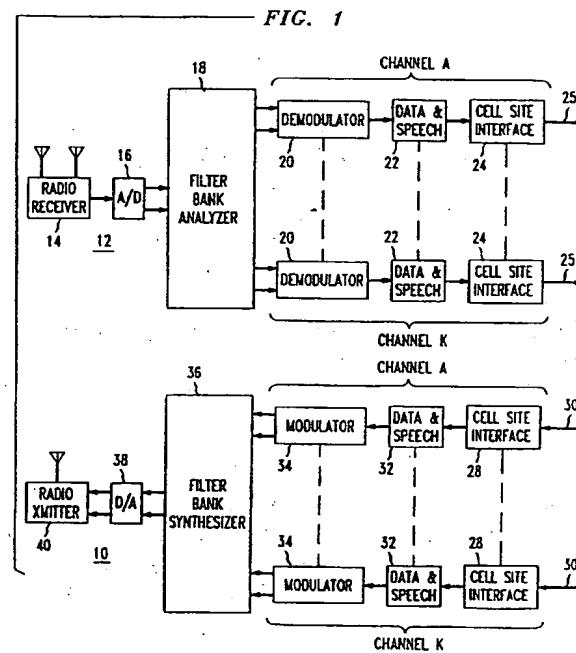
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(54) Frequency-multiplexed cellular telephone cell site base station and method of operating the same.

(57) The present invention is directed to a cellular telephone cell site base station (10) having a single radio transmitter (40) and radio receiver (14) for transmitting and receiving, respectively, multiplexed frequency-modulated (FM) signals. A filter bank synthesizer (36), associated with the transmitter, multiplexes individual signals for transmission by the transmitter by exponentially modulating the signals to higher frequency bands followed by low-pass filtering and interpolation using discrete Fourier transform techniques. A filter bank analyzer (18), associated with the receiver, demultiplexes multiplexed FM signals received by the receiver by exponentially modulating each signal to its original spectral position followed by low-pass filtering and decimation, using discrete Fourier transform techniques.



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Technical Field

This invention relates to a cellular telephone cell site base station for transmitting and receiving frequency-multiplexed signals using a single transmitter and receiver, respectively.

Background of the Invention

Cellular telephony is based on the concept of dividing a geographical area into a plurality of individual sub-areas or "cells." Situated within each cell is a base station containing at least one receiver and transmitter tuned to a particular frequency (channel) for communicating with a mobile transceiver (i.e., terminal), also tuned to receive and transmit on that channel. In actuality, the receive and transmit portions of the channel are spaced 45 MHz apart. Typically, the mobile terminal is carried in a vehicle in transit within that cell. As the vehicle containing the mobile terminal travels from one cell into another, the call between the mobile terminal and the cell site base station is transferred to a base station of the new cell. Typically, the new cell site base station has its receiver and transmitter tuned to a different channel, necessitating that the mobile terminal change its reception and transmission channel as well. The purpose in assigning a different channel to adjacent cells is to allow distant cells to use the same channel, thereby increasing the effective number of channels.

To facilitate more than a single call within each cell, a cell site base station must have the ability to receive and transmit calls on different channels. At the present time, such a capability is obtained by providing the cell site with multiple transmitter-receiver pairs, each tuned to a particular channel. The disadvantage of this approach is that the cost of providing such equipment rises in direct proportion to the number of channels to be offered. Moreover, each receiver-transmitter pair occupies a certain amount of physical space which is often at a premium within the cell site itself.

Thus, there is a need for a cellular telephone system which can transmit and receive multiple channel using a single transmitter and receiver pair.

Summary of the Invention

Briefly, in accordance with a preferred embodiment of the invention, there is provided a cellular telephone cell site base station for transmitting and receiving frequency-multiplexed signals as recited in Claim 1.

Brief Description of the Drawing

FIGURE 1 is a block schematic diagram of a cellular telephone cell site base station in accor-

dance with the invention;

FIGURE 2 block schematic diagram of a filter bank analyzer comprising a part of the base station of FIG. 1; and

FIGURE 3 is a block schematic of a filter bank synthesizer comprising a part of the base station of FIG. 1.

Detailed Description

FIGURE 1 is a block schematic diagram of a cellular telephone cell site base station 10 for transmitting and receiving a frequency-multiplexed base-band signal in accordance with the invention. However, as will be appreciated from the following discussion, the invention may be equally useful for other types of radio transceiver systems. The base station 10 is comprised of a receiver section 12 for receiving a frequency-multiplexed signal comprised of K individual base-band signals (channels) where K is an integer. Typically, each individual base-band channel within the multiplexed signal received by the receiver section 12 is transmitted by a separate one of a plurality of mobile cellular telephone terminals (not shown). In practice, the channels in the frequency-multiplexed signal are spaced 30 kHz apart although other spacings could be used. The receiver section 12 comprises a radio receiver 14 which has the capability of receiving the K-channel, frequency-multiplexed signal.

The radio receiver 14 receives the frequency-multiplexed signal at Radio Frequency (RF) and, in response, generates an analog frequency-multiplexed signal at a baseband frequency. The output signal from the receiver 14 is supplied to an analog-to-digital conversion device 16 which converts the analog signal from the receiver 14 into a digital signal that is input to a filter bank analyzer 18. As will be described with respect to FIG. 2, the frequency bank analyzer 18 exponentially modulates the digital signal, then low-pass filters and decimates such signals to yield K complex signals, each corresponding to a particular channel transmitted by an individual mobile terminal.

Each of the K complex signals produced by the frequency bank analyzer 18 is supplied to a separate one of K demodulation channels 1,2...K. Each demodulation channel includes a demodulator 20 for demodulating the signal supplied to it in accordance with the manner in which the signal was modulated. When the received signal is frequency modulated, each of the K channels of the frequency-multiplexed signal received at the demodulator 20 is constructed to demodulate such an FM signal by computing the angle difference ($\tan^{-1}(I/Q)$), where I and Q are the angles of the in-phase and quadrature phase components of the channelized signal, respectively. Alternatively, the originally received frequency-modulated signal may be time-division modulated (TDMA), in which

case each demodulator 20 is configured to demodulate such TDMA signals.

Still referring to FIG. 1, each of the K demodulation channels also includes a data and speech circuit for processing the output signal produced by the demodulator 20 to enhance the voice (or data) portion of the signal. Typically, the data and speech circuit 22 includes a digital signal processor (not shown). The manner in which the data and speech circuit 22 processes the output signal of the demodulator depends on the manner in which the demodulator 22 demodulates the signal input to it.

In the case where the demodulator 22 receives an FM signal, the data and speech circuit 22 processes the demodulator output signal by performing the following operations thereon: (1) high pass filtering, (2) decimation with low-pass filtering, (3) expansion, and (4) de-emphasis. Such processing steps are further described in my U.S. Patent No. 5,001,742, issued March 19, 1991, and assigned to AT&T Bell Laboratories, such patent herein incorporated by reference.

In practice, each separate one of the K channels of the K channels of the originally received, frequency-multiplexed signal has a Supervisory Audio Tone (SAT) signal imposed thereon by a separate mobile terminal for the purpose of establishing whether the terminal is in contact with the cell site base station 10. For this reason, each data and speech circuit 22 is also provided with the capability of processing the SAT signal superimposed on incoming channel in the manner disclosed in my aforementioned U.S. patent 5,001,742. The output signal of the data and speech circuit 22 is input to a cell site trunk interface circuit 24, as are known in the art, which serves to interface the data and speech circuit output signal (which is either a voice or data signal) for input to an outgoing telephone trunk line 25.

In the case where the signal input to the demodulator 20 is TDMA modulated, the data and speech circuit 22 serves to perform the steps of channel decoding and then speech decoding in a well-known manner.

In addition to the receiver section 12 thus described, the cell site base station 10 of the present invention also includes a transmitter section 26 which, as will be described, operates to frequency-multiply K individual voice signals and to transmit such a frequency-multiplexed signal containing the K individual channels to one or more mobile terminals. The transmitter section 26 comprises K separate modulation channels 1,2...K, each including a cell site interface circuit 28, similar to the circuit 24, for interfacing the modulation channel to an incoming telephone trunk line 30 which carries an incoming voice or data signal. The interface circuit 28 of each modulation channel is coupled to a data and speech circuit 32, typically comprised of a digital signal processor (not shown).

Each data and speech circuit 32 serves to process the incoming voice or data signal from the cell site interface circuit 28 depending on the manner in which the signal is to be modulated for ultimate transmission. In the case where signals are to be frequency modulated, then, the data and speech circuit 28 processes the incoming voice or data signal by performing the following steps: (1) high-pass filtering, (2) compression, (3) pre-emphasis, (4) limiting the signal, and (5) interpolating and low-pass filtering the signal. For a further description of the manner in which such steps are carried out, reference should be had to my aforementioned U.S. patent 5,001,742, herein incorporated by reference.

As indicated earlier, it is desirable for each mobile terminal in communication with the cell site 10 base station to superimpose an SAT signal on the voice or data signal it transmits. By the same token, it is desirable for the cell site base station 10 to superimpose an SAT signal on each channel transmitted to a particular mobile terminal. Accordingly, each data and speech circuit 32 is provided with the capability of superimposing an SAT signal on the voice or data signal processed thereby in the manner described in my aforementioned U.S. Patent 5,001,742.

If the signal ultimately to be transmitted is to be modulated in a different manner, then the manner in which the data and speech circuit 32 processes the incoming voice or data signal will be different. When the signal input to the data and speech circuit 22 is to be subsequently TDMA modulated, then the data and speech circuit first codes the incoming voice (or data) signal and thereafter codes the channel in a well-known manner.

The output of the data and speech circuit 32 of each modulation channel is input to a modulator 34 designed to modulate the signal in a particular format, say FM or TDMA, depending on the manner in which the signal supplied from the data and speech circuit had been processed. The signals from the modulators 34 of the modulation channels are input to a filter bank synthesizer 36. As will be better described with respect to FIG. 3, the filter bank synthesizer 36 serves to exponentially modulate the individual signals from the modulators 34, followed by low-pass filtering, interpolation and summing, to yield a K-channel frequency-multiplexed signal having digitized in-phase and quadrature phase components. A digital-to-analog conversion device 38 (typically comprised of a pair of D/A converters) serves to convert the digital in-phase and quadrature phase components of the frequency bank synthesizer 36 output signal into a corresponding pair of analog signals which are input to a radio transmitter 40. The transmitter 40 combines the analog in-phase and quadrature-phase components of the synthesized K-channel frequency-multiplexed signal and then transmits the combined signals for reception by one or more mobile

terminals (not shown).

Referring to FIG. 2, there is shown a block schematic diagram of a symbolic representation of the filter bank analyzer 36 of FIG. 1. As shown in FIG. 2, the filter bank analyzer 36 is comprised of K separate channels 1,2...K. Each channel includes a complex modulator 42 which serves to exponentially modulate the real and quadrature phase signal components supplied to the filter bank synthesizer by the function $e^{-j\omega_k t}$ where:

$$\omega_k = 2\pi k/K, k = 1,2,\dots,K-1 \quad (1)$$

By exponentially modulating the incoming signal in this fashion, each complex modulator 42 serves to shift a separate one of the K channels of originally received signal to its original spectral position.

Each of the K channels of the filter bank analyzer 36 of FIG. 2 also includes a low-pass filter 44 for low-pass filtering the output signal of the modulator 42. A decimator 46 serves to decimate the output signal of the low-pass filter 44 by a factor of M, where M is a constant, so that the output signal of the decimator, representing a separate one of the K channels of the original frequency-multiplexed signal, is returned to its original sampling rate.

In practice, the frequency bank analyzer 18 is typically not implemented by means of separate discrete components, but rather, the analyzer is comprised of a digital signal processor (not shown) which is programmed to carry out the steps of (a) exponential modulation, (b) low-pass filtering, and (c) decimation using fast Fourier transform techniques as are described at pages 289-310 of the text *Multirate Digital Signal Processing* by Ronald E. Crochiere and Lawrence R. Rabiner (Prentice Hall, 1983), herein incorporated by reference. To compensate for the fact that the signals input to the filter bank analyzer 18 were originally obtained from analog signals of a continuous nature, the analyzer typically weights the signals, using an empirically established weighting factor, prior to processing.

Referring now to FIG. 3, there is shown a block schematic diagram of a symbolic representation of the filter bank synthesizer 36 of FIG. 1. As seen in FIG. 3, the filter bank synthesizer is comprised of K individual channels 1,2,3...K. Each channel includes an interpolator 48 for interpolating a signal received from a corresponding modulator 44 of FIG. 1 by the factor of M. The output signal of the interpolator 48 of the channel is low-pass filtered by a low-pass filter 50 before being input to a complex modulator 52 which serves to modulate the low-pass filtered signal by $e^{-j\omega_k t}$, where ω_k is given in eq. 1. The output signal of the complex modulator 52 of each channel is summed by a summing amplifier 54 to yield a frequency-multiplexed signal comprised of K individual channels.

As with the frequency bank analyzer 18 of FIG. 2, the frequency bank synthesizer 36 of FIG. 3 is implemented in terms of a digital signal processor (not

shown) programmed to perform the steps of (1) interpolation, (2) low-pass filtering, and (3) complex modulation using inverse fast Fourier transform techniques. For a further description of how such steps may be carried out using inverse fast Fourier techniques, reference should be had to the aforementioned text *Multirate Digital Signal Processing* by Ronald E. Crochiere and Lawrence R. Rabiner incorporated by reference herein.

The foregoing describes a cellular telephone cell site base station 10 which advantageously utilizes a single transmitter 40 and a single receiver 14 for transmitting and receiving, respectively, a frequency-multiplexed signal containing K separate channels, each channel representing a signal associated with a separate mobile terminal. By frequency multiplexing and de-multiplexing K separate channels using the filter bank synthesizer 36 and filter bank analyzer 18, respectively, the need for multiple transmitter-receiver pairs is obviated, thereby achieving a savings in terms of both cost and space.

It is to be understood that the above-described embodiments are merely illustrative of the principles of the invention. Various modifications and changes may be made thereto by those skilled in the art which will embody the principles of the invention and fall within the spirit and scope thereof.

Claims

1. A radio transceiver system CHARACTERIZED BY:

a receiver (14) for receiving a frequency-multiplexed signal containing K individual channels (where K is an integer) and for generating output signals indicative of the received signal;

first signal-processing means (16,18) for processing the output signals of the receiver to separate each of the K channels in the originally-received frequency-multiplexed signal;

a plurality of K demodulators (20), each demodulating a separate one of the K channels separated by the first signal-processing means;

a first plurality of K data and speech-processing circuits (22), each serving to process the demodulated channel produced by a separate demodulator in accordance with the manner in which the channel was demodulated to yield a separate one of K incoming voice/data signals;

a second plurality of K data and speech-processing circuits (32), each processing a separate one of K outgoing voice/data signals in a predetermined manner;

a plurality of K modulators (34), each modulating a separate one of the K outgoing voice/data signals in a particular manner in accordance with the way in which such signals were

- processed by a corresponding one of the second speech and data circuits;
- second signal-processing means (36,38) for multiplexing the output signals of the K modulators to yield a frequency-multiplexed signal having K channels; and
- a transmitter (40) for transmitting the frequency-multiplexed signal generated by the second signal-processing means.
2. The apparatus according to claim 1 CHARACTERIZED IN THAT the first signal processing means comprises:
- analog-to-digital conversion means (16) for converting the receiver output signals to digital signals; and
- filter bank analyzer means (18) for (1) exponentially modulating, (2) low-pass filtering, and (3) decimating the digitized receiver output signals to demultiplex the originally received signal to yield K separate channels.
3. The apparatus according to claim 2 CHARACTERIZED IN THAT each first data and data processing circuit comprises a digital signal processor programmed to processes the output signal of each demodulator by (1) high-pass filtering, (2) decimation with low-pass filtering, (3) expansion, and (4) de-emphasis.
4. The apparatus according to claim 1 CHARACTERIZED IN THAT the second signal-processing means comprises:
- filter bank synthesizer means (36) for frequency-multiplexing the K modulator output signals by (1) separately interpolating each signal, (2) low-pass filtering each signal, (3) exponentially modulating each low-pass signal to a higher frequency band, and (4) summing the individually exponentially modulated signals to obtain a complex K channel frequency-multiplex signal; and
- digital-to-analog conversion means (38) for converting the filter bank synthesizer output signal to an analog signal.
5. The apparatus according to claim 4 CHARACTERIZED IN THAT each second data and speech-processing circuit comprises a digital signal processor programmed to process a signal for input to the modulator by: (1) high-pass filtering, (2) compression, (3) pre- emphasis, (4) limiting the signal, and (5) interpolating and low pass filtering the signal.
6. A method for processing an incoming frequency-multiplexed K channel signal containing voice and/or data to separate each of the K channels, CHARACTERIZED BY the steps of:
- receiving the incoming K-channel signal and generating a pair of complex signals indicative of the in-phase and quadrature phase components thereof;
- converting each of the complex signals to digitized signals;
- exponentially modulating each of the digitized complex signals to yield K separate channels;
- low-pass filtering each of the K channels; decimating each of the K channels; demodulating each of the K channels; and processing each of the K channels in accordance with the manner in which the channels were demodulated.
7. The method according to claim 6 CHARACTERIZED IN THAT each of the K channels is frequency-demodulated.
8. The method according to claim 6 CHARACTERIZED IN THAT each of the K channels is TDMA demodulated.
9. A method for combining K outgoing voice and/or data signals to yield a frequency-multiplexed K channel signal, CHARACTERIZED BY the steps of:
- processing each of the K incoming signals;
- modulating each of the processed K incoming signals;
- exponentially modulating the processed K incoming signals;
- low-pass filtering the exponentially modulated signals;
- interpolating the low-pass-filtered signals; and
- summing the interpolated signals to yield the frequency-multiplexed K channel signal.
10. The method according to claim 9 CHARACTERIZED IN THAT each of the K incoming signals is frequency modulated.
11. The method according to claim 9 CHARACTERIZED IN THAT each of the incoming signals is TDMA modulated.

FIG. 1

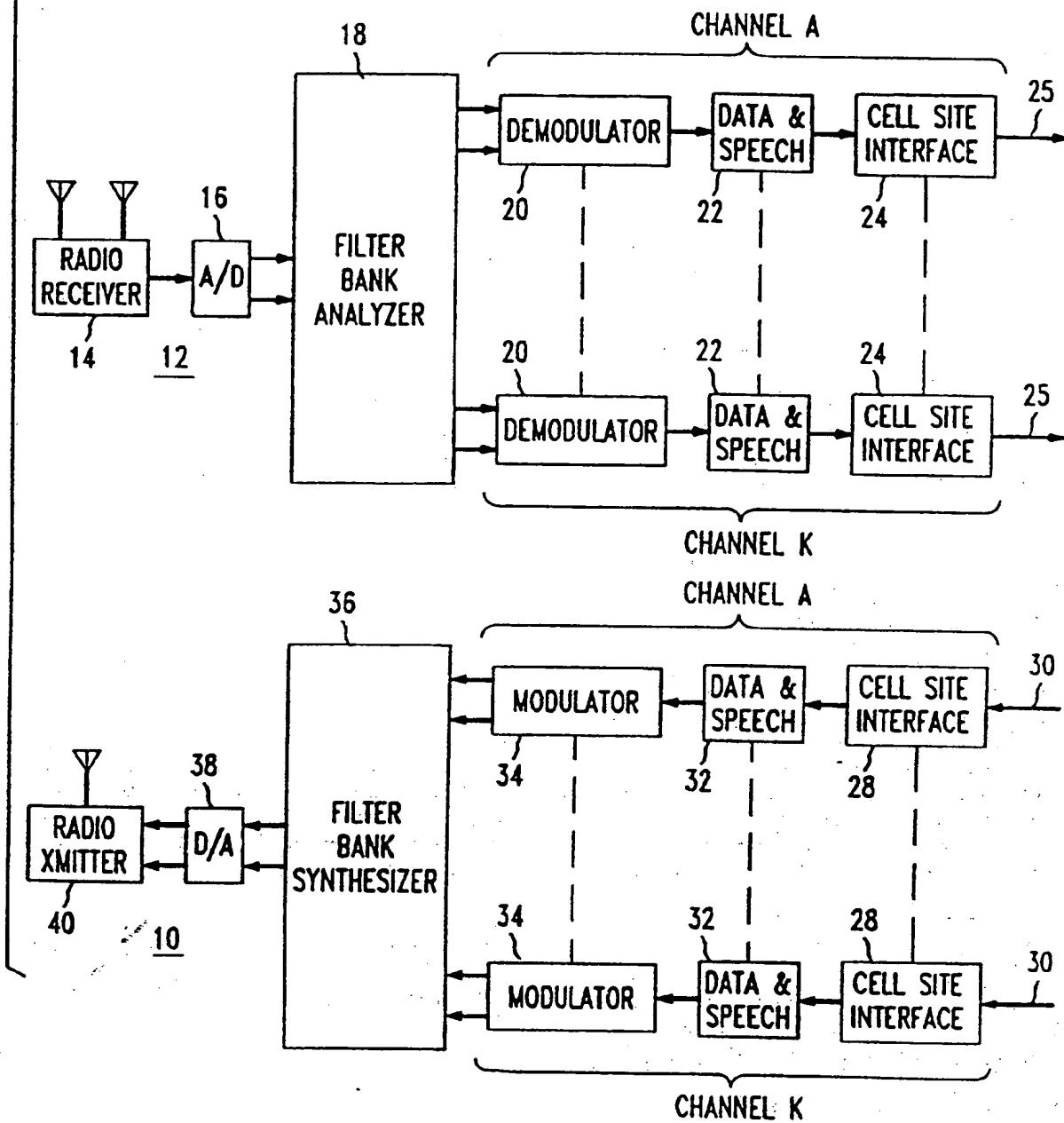
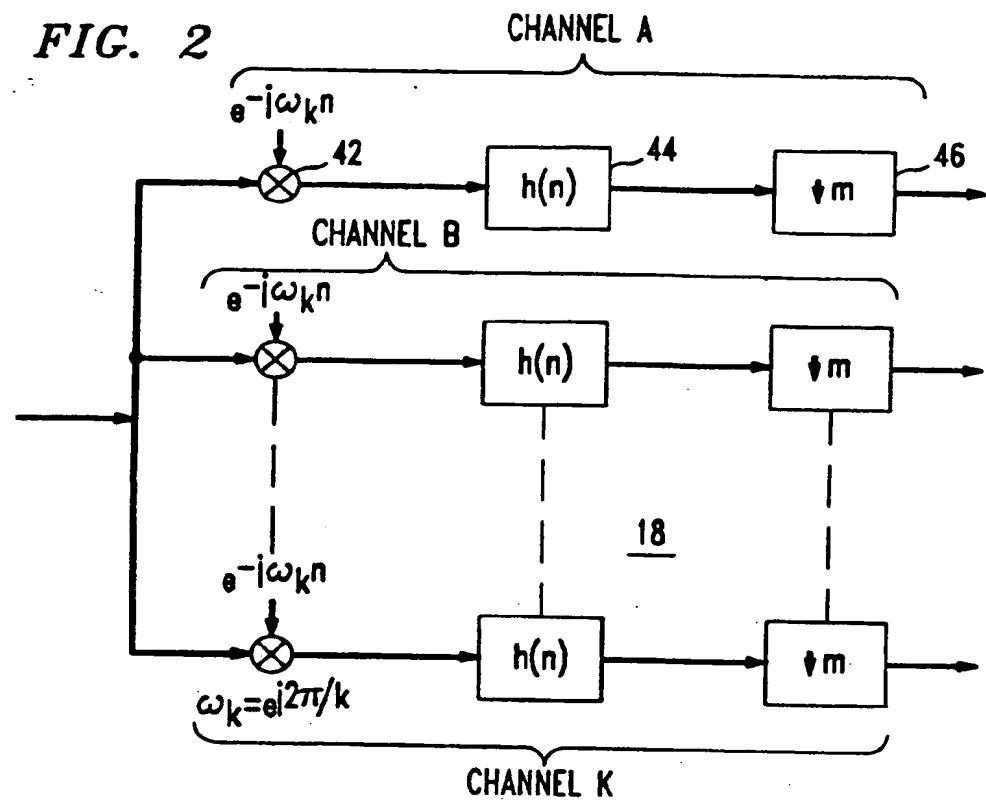
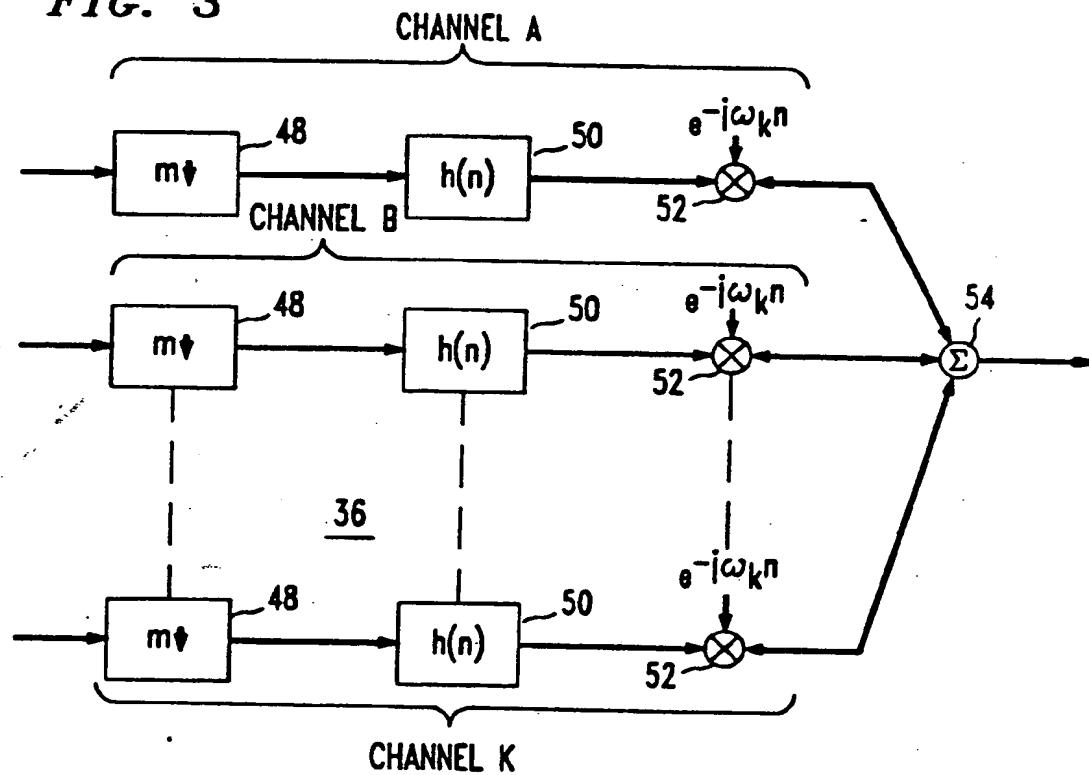


FIG. 2**FIG. 3**



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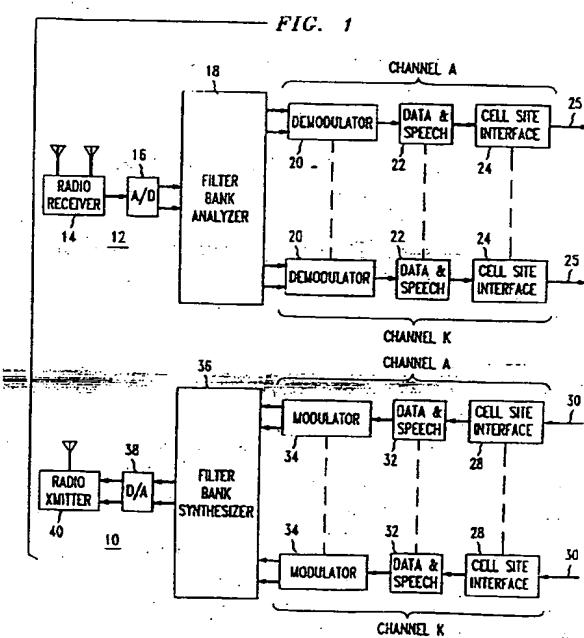
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EUROPEAN SEARCH REPORT

Application Number
EP 93 30 7104

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CLS)
X	EP-A-0 377 509 (HUGHES AIRCRAFT) * abstract * * column 3, line 30 - line 50 * * column 7, line 16 - column 9, line 44; claims 1-4; figures 1,4,9 *	6-8	H04B7/26 H04Q7/04 H04J1/02
A	---	1,2	
D, Y	CROCHIERE ET AL. 'Multirate digital Signal Processing' 1986 , PRENTICE-HALL , US * page 290, line 1 - page 292, line 6; figure 7.3 * * paragraph 7.2.1 *	1-11	
Y	---	1-11	
	FUNKSCHAU, vol.59, no.19, 11 September 1987, MUNCHEN DE pages 42 - 45 'Auf dem Weg zum D-Netz' * page 42, middle column, line 1 - page 44, left column, line 14; figure 2 *		
A	EP-A-0 466 139 (KOKUSAI DENSHIN DENWA) * page 6, line 16 - line 42; figures 1,3 *	1	TECHNICAL FIELDS SEARCHED (Int.Cl.5)
A	PHILIPS TELECOMMUNICATION REVIEW, vol.50, no.2, August 1992, HILVERSUM NL pages 78 - 83 MCMAHON 'Base Staion Control using DSP Techniques' * the whole document *	3,5	H04Q H04L H04B H04J
P, X	US-A-5 208 804 (WILSON ET AL.) * column 3, line 66 - column 4, line 57; claims 1,4,7,11,16,17; figures 1,2 *	1,3,5	-

The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	22 August 1994	Janyszek, J-M	
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